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*Opal, A.;*

Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on

Volume: 15 Issue: 3, Mar 1996

Page(s): 295 -307

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## 17 Analysis of switched-capacitor and switched-current networks with complete settling assumption

*Suyama, K.; Yustin, I.;*

Circuits and Systems, 1996. ISCAS '96., 'Connecting the World', 1996 IEEE

International Symposium on, Volume: 4, 12-15 May 1996

Page(s): 428 -431 vol.4

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## 18 A rapidly convergent technique for determining the response of AC/D converters

*Naidu, S.R.; Trindade, W.J.;*

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## 19 Analogue implementation of a neural network controller for UPS inverter applications

*Chow, M.H.L.; Dehong Xu; Leung, F.H.F.; Xiao Sun; Yim-Shu Lee; Yousheng Wang*

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**20 Duomode non-linear state space averaged SPICE model of a current mode buck converter**

*Chen, J.E.; Rodriguez, F.D.;*

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Page(s): 53 -58

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*Costiner, S.; Ioinovici, A.;*

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Page(s): 1098 -1101 vol.2

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**22 Control of front-end three-phase boost rectifier**

*Boroyevich, D.; Hiti, S.;*

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Page(s): 927 -933 vol.2

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Page(s): 339 -342 vol.1

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**24 Comparison of fuzzy logic and current-mode control techniques in buck boost and buck/boost converters**

*Fang Hsien Wang; Lee, C.Q.;*

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Page(s): 1079 -1085 vol.2

**25 PWM AC/DC converter with reduced sampling frequency fuzzy contro**  
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**26 Simulator for switched-current integrated circuits**  
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Page(s): 459 -462

**27 Simulation of electric power circuit breakers in the EMTP incorporatin**  
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Page(s): 1337 -1349

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Page(s): 1829 -1835

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Page(s): 3146 -3149

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[\[Prev\]](#) [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [\[Next\]](#)

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-	403	"TOSHIBA TEC KABUSHIKI KAISHA".as.	USPAT; US-PGPUB	2002/12/20 11:35
-	492585	simulat\$4 or model\$3 or emulat\$4	USPAT; US-PGPUB	2002/12/20 11:40
-	16	"TOSHIBA TEC KABUSHIKI KAISHA".as. and (simulat\$4 or model\$3 or emulat\$4)	USPAT; US-PGPUB	2002/12/20 11:36
-	169333	(simulat\$4 or model\$3 or emulat\$4) and circuit	USPAT; US-PGPUB; IBM_TDB	2002/12/20 11:41
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-	53287	((((simulat\$4 or model\$3 or emulat\$4) and circuit) and volt\$4 and current and (resist\$4 or conduct\$4)) and (line\$5 or nonline\$5 or non-lin\$5))	USPAT; US-PGPUB; IBM_TDB	2002/12/20 11:46
-	50248	(((((simulat\$4 or model\$3 or emulat\$4) and circuit) and volt\$4 and current and (resist\$4 or conduct\$4)) and (line\$5 or nonline\$5 or non-lin\$5)) and (time or timing)	USPAT; US-PGPUB; IBM_TDB	2002/12/20 11:51
-	286196	((wire or wiring) same (connect\$4 or intersect\$4))	USPAT; US-PGPUB; IBM_TDB	2002/12/20 11:51
-	14867	(((((simulat\$4 or model\$3 or emulat\$4) and circuit) and volt\$4 and current and (resist\$4 or conduct\$4)) and (line\$5 or nonline\$5 or non-lin\$5)) and (time or timing) and (((wire or wiring) same (connect\$4 or intersect\$4))))	USPAT; US-PGPUB; IBM_TDB	2002/12/20 12:20
-	3948	(((((simulat\$4 or model\$3 or emulat\$4) and circuit) and volt\$4 and current and (resist\$4 or conduct\$4)) and (line\$5 or nonline\$5 or non-lin\$5)) and (time or timing) and (((wire or wiring) same (connect\$4 or intersect\$4)))) and (electron or particle)	USPAT; US-PGPUB; IBM_TDB	2002/12/20 12:02
-	3249	(((((simulat\$4 or model\$3 or emulat\$4) and circuit) and volt\$4 and current and (resist\$4 or conduct\$4)) and (line\$5 or nonline\$5 or non-lin\$5)) and (time or timing) and (((wire or wiring) same (connect\$4 or intersect\$4)))) and (electron or particle)) and state	USPAT; US-PGPUB; IBM_TDB	2002/12/20 12:03
-	2112	((((((simulat\$4 or model\$3 or emulat\$4) and circuit) and volt\$4 and current and (resist\$4 or conduct\$4)) and (line\$5 or nonline\$5 or non-lin\$5)) and (time or timing) and (((wire or wiring) same (connect\$4 or intersect\$4)))) and (electron or particle)) and state) and transfer\$5	USPAT; US-PGPUB; IBM_TDB	2002/12/20 12:03

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-	1578	(((((((((simulat\$4 or model\$3 or emulat\$4) and circuit) and volt\$4 and current and (resist\$4 or conduct\$4)) and (line\$5 or nonlinear\$5 or non-lin\$5)) and (time or timing) and ((wire or wiring) same (connect\$4 or intersect\$4)))) and (electron or particle)) and state) and transfer\$5) and (rule or law or guide or standard)) and source	USPAT; US-PGPUB; IBM_TDB	2002/12/20 12:05
-	1259	(((((((((simulat\$4 or model\$3 or emulat\$4) and circuit) and volt\$4 and current and (resist\$4 or conduct\$4)) and (line\$5 or nonlinear\$5 or non-lin\$5)) and (time or timing) and ((wire or wiring) same (connect\$4 or intersect\$4)))) and (electron or particle)) and state) and transfer\$5) and (rule or law or guide or standard)) and source) and (computer or cpu or processor or microprocessor)	USPAT; US-PGPUB; IBM_TDB	2002/12/20 12:08
-	1210	(((((((((simulat\$4 or model\$3 or emulat\$4) and circuit) and volt\$4 and current and (resist\$4 or conduct\$4)) and (line\$5 or nonlinear\$5 or non-lin\$5)) and (time or timing) and ((wire or wiring) same (connect\$4 or intersect\$4)))) and (electron or particle)) and state) and transfer\$5) and (rule or law or guide or standard)) and source) and (computer or cpu or processor or microprocessor)) and (unit or cell or node)	USPAT; US-PGPUB; IBM_TDB	2002/12/20 12:13
-	2406	(simulat\$4 and circuit).ab.	USPAT; US-PGPUB; IBM_TDB	2002/12/20 16:42
-	0	(((((((((simulat\$4 or model\$3 or emulat\$4) and circuit) and volt\$4 and current and (resist\$4 or conduct\$4)) and (line\$5 or nonlinear\$5 or non-lin\$5)) and (time or timing) and ((wire or wiring) same (connect\$4 or intersect\$4)))) and (electron or particle)) and state) and transfer\$5) and (rule or law or guide or standard)) and source) and (computer or cpu or processor or microprocessor)) and (unit or cell or node)) and ((simulat\$4 and circuit).ab.)	USPAT; US-PGPUB; IBM_TDB	2002/12/20 12:14
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-	385	((simulat\$4 and circuit).ab.) and (volt\$4 and current and (resist\$4 or conduct\$5 or imped\$4)) and transfer\$5	USPAT; US-PGPUB; IBM_TDB	2002/12/20 12:19
-	303	((simulat\$4 and circuit).ab.) and (volt\$4 and current and (resist\$4 or conduct\$5 or imped\$4)) and transfer\$5) and state	USPAT; US-PGPUB; IBM_TDB	2002/12/20 12:20

-	141	(((((simulat\$4 and circuit).ab.) and (volt\$4 and current and (resist\$4 or conduct\$5 or imped\$4))) and transfer\$5) and state) and (linear\$3 or nonlinear\$3 or non-linear\$3)	USPAT; US-PGPUB; IBM_TDB	2002/12/20 12:21
-	140	(((((simulat\$4 and circuit).ab.) and (volt\$4 and current and (resist\$4 or conduct\$5 or imped\$4))) and transfer\$5) and state) and (linear\$3 or nonlinear\$3 or non-linear\$3)) and (time or timing)	USPAT; US-PGPUB; IBM_TDB	2002/12/20 16:41